PMC-APN020 P003x Version Differences

P003x Version Differences

1. Purpose

Describe the main differences between versions of P003(X) Writer

2. Application scope

P003 P003B P003B1 P003B2 P003C

3. List of major differences

List of differences	P003	P003B	P003B1	P003B2	P003C
Expansion of Writing Data Memory (using SPI_Flash Memory)		V	V	V	V
LCM Independent 5V		√	√	√	√
DC Input power over-voltage protection Writer Power Fuse (FUSE) Protection		V	V	V	~
Optimizing some chips for writing requires VDD and PA5 jumpers.			V	V	V
ESD protection component added to the main control IC pins			V	V	V
Optimized OS detection circuit				$\sqrt{}$	$\sqrt{}$
Addition of ESD Protection Component (1) Writing Block Pins(Socket) (2) USB port (USB) (3) semi-automatic interface(Handler port)					V
Master IC IO pin current limiting resistor protection Master IC DAC pin current limiting resistor protection					√

If you have any questions to the application, please consult our agent at your nearest location or contact us at fae@padauk.com.tw.